

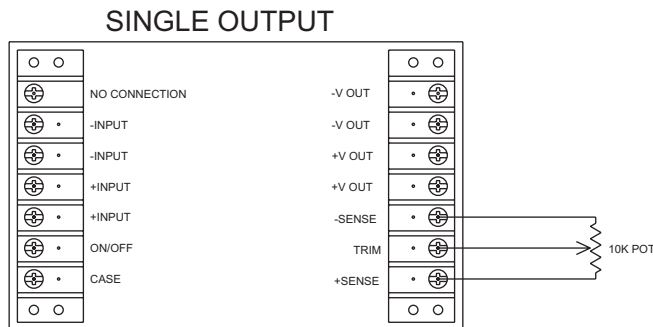


OUTPUT TRIM FOR "D" - SINGLE OUTPUT

Method 1 - Trim Potentiometer

This method is the simplest method for trimming a D Series DC/DC Converter. Connect a 10 kilo-ohm potentiometer as shown in Figure I. This will allow, typically, a $\pm 10\%$ trim of the output voltage. For maximum trim in either direction, connect the TRIM pin directly to the appropriate SENSE pin.

Figure I - Trim Potentiometer



Method 2 - Trim Resistor

This method is the most basic method of output trimming for the D Series DC/DC Converter. To trim the voltage up, connect a resistor from the TRIM pin to the -SENSE pin of the converter. This is shown in Figure II. To trim the voltage down, connect a resistor from the TRIM pin to the +SENSE pin of the converter. This is shown in Figure III. For maximum trim in either direction, connect the TRIM pin directly to the appropriate SENSE pin. Typically, the maximum trim for the output voltage is $\pm 10\%$.

Figure II - Trim Up Resistor

SINGLE OUTPUT

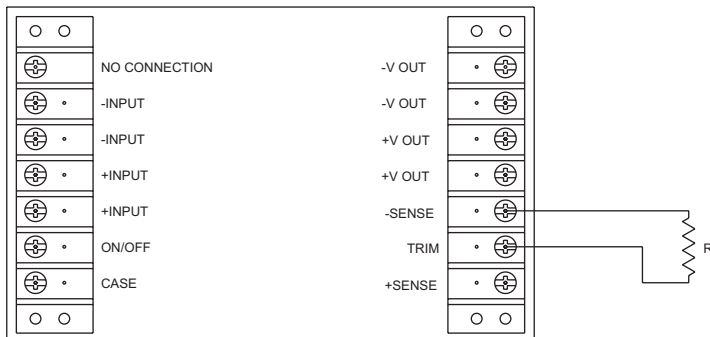
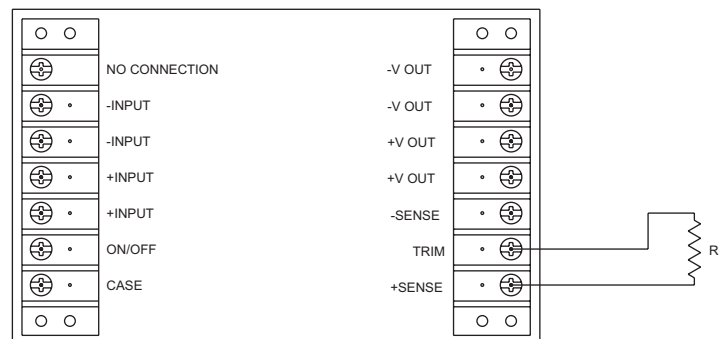


Figure III - Trim Down Resistor

SINGLE OUTPUT



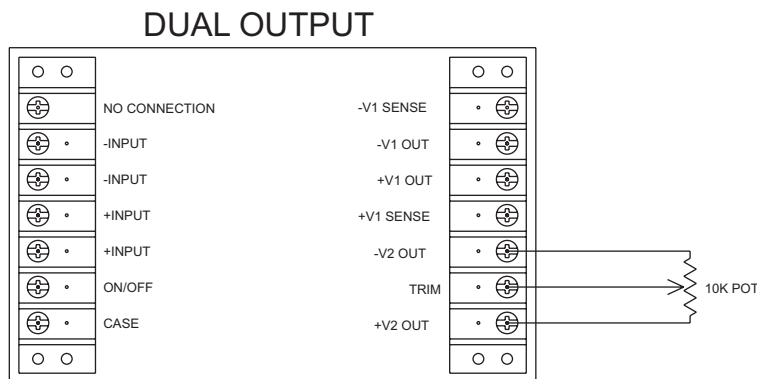


OUTPUT TRIM FOR "D" - DUAL OUTPUT

Method 1 - Trim Potentiometer

This method is the simplest method for trimming a D Series DC/DC Converter. Connect a 10 kilo-ohm potentiometer as shown in Figure I. This will allow, typically, a $\pm 10\%$ trim of the output voltage. For maximum trim in either direction, connect the TRIM pin directly to the appropriate V2 OUT pin.

Figure I - Trim Potentiometer



Method 2 - Trim Resistor

This method is the most basic method of output trimming for the D Series DC/DC Converter. To trim the voltage up, connect a resistor from the TRIM pin to the -V2 OUT pin of the converter. This is shown in Figure II. To trim the voltage down, connect a resistor from the TRIM pin to the +V2 OUT pin of the converter. This is shown in Figure III. For maximum trim in either direction, connect the TRIM pin directly to the appropriate V2 OUT pin. Typically, the maximum trim for the output voltage is $\pm 10\%$.

Figure II - Trim Up Resistor

DUAL OUTPUT

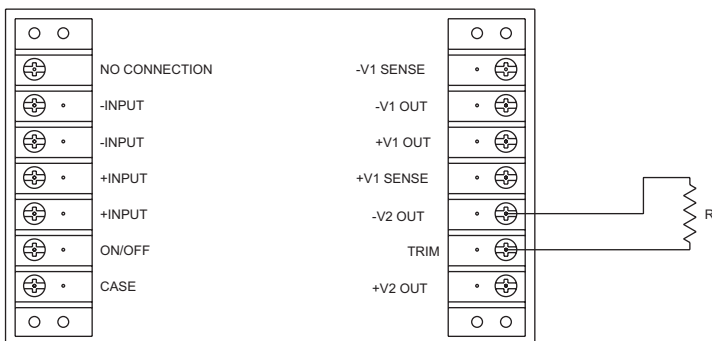


Figure III - Trim Down Resistor

DUAL OUTPUT

